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## APPLICANTS

Kouzi Hayasi, Kanagawa, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

N/A

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-288777 10/01/2002

yes.

IF REQUIRED, FOREIGN FILING LICENSE  
GRANTED

\*\* 12/22/2003

Sflu  
12/20/04

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance Examiner's Signature	STATE OR  COUNTRY JAPAN	SHEETS  DRAWING	TOTAL  CLAIMS	INDEPENI  CLAIM
	Initials		25	13	2

## ADDRESS

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## TITLE

Semiconductor device including bipolar junction transistor, and production method therefor

☐ All Fees☐ 1.16 Fees ( Filing )